

4. Linear Page Table

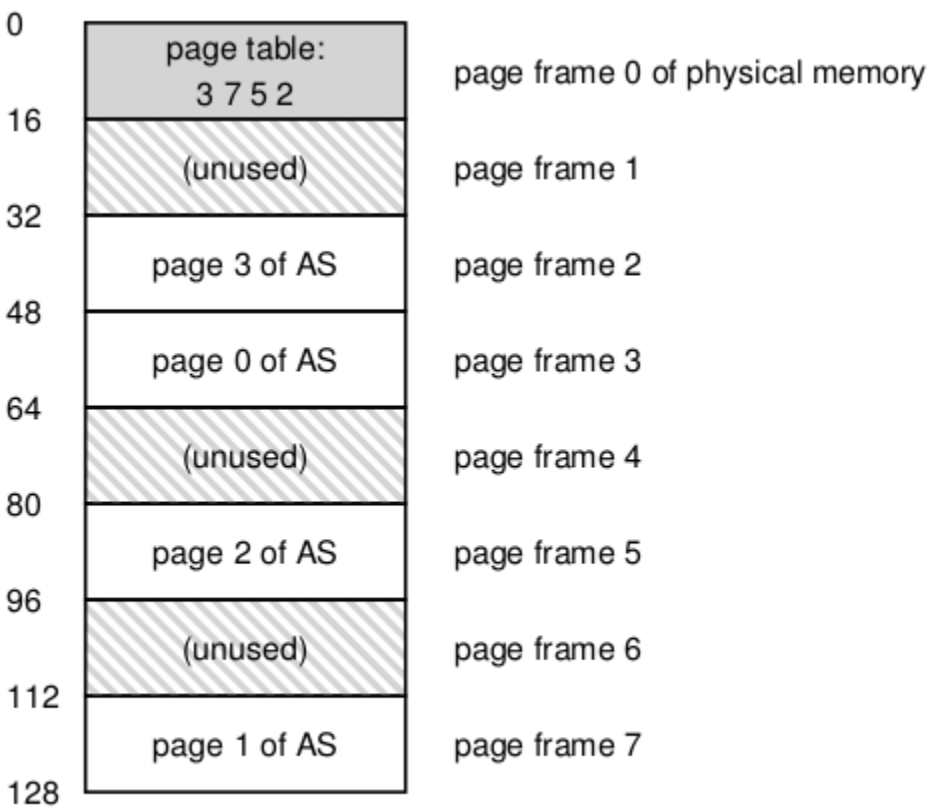


Figure 18.4: Example: Page Table in Kernel Physical Memory

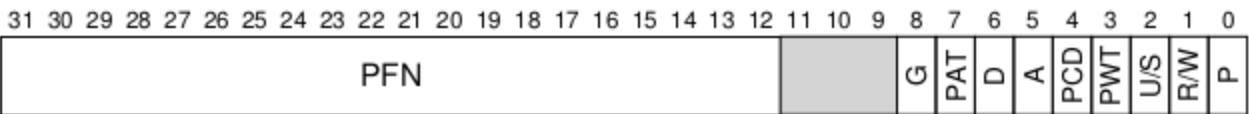


Figure 18.5: An x86 Page Table Entry (PTE)