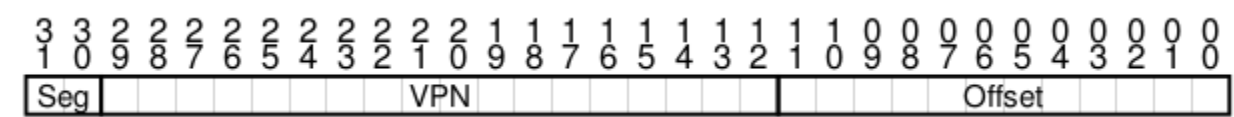


2. Segment Tables

Base and bounds registers for each segment

Example: 32-bit virtual address space, 4KB pages, three segments: code, heap, stack

2-bit segment selector: 00 unused, 01 code, 10 heap, 11 stack



TLB miss handling:

$$SN = (VirtualAddress \& SEG_MASK) \gg SN_SHIFT$$

$$VPN = (VirtualAddress \& VPN_MASK) \gg VPN_SHIFT$$

$$AddressOfPTE = Base[SN] + (VPN * sizeof(PTE))$$

specify SEG_MASK, SN_SHIFT, VPN_MASK, VPN_SHIFT: _____